



PATENT APPLICATION
Docket No. 8750-040
Client No. SPX200211-00473US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Jae-Hun Kim and Kyu-Whan Chong

Serial No. 10/666,507 Examiner: Perkins, Pamela E.
Filed: September 19, 2003 Art Unit: 2822
For: MOS TRANSISTOR HAVING A RECESSED GATE
ELECTRODE AND FABRICATION METHOD THEREOF
Confirmation No. 9186

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

TRANSMITTAL LETTER

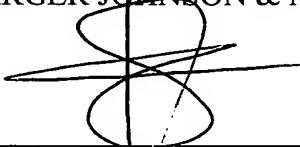
Enclosed for filing in the above-referenced application are the following:

Response to Restriction Requirement
 Any deficiency or overpayment should be charged or credited to deposit account number 13-1703.

Customer No. 20575

Respectfully submitted,

MARGER JOHNSON & McCOLLUM, P.C.

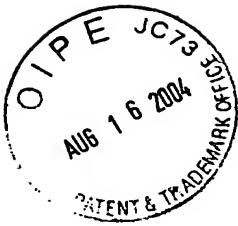


Kevin S. Ross
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22313-1450
Date: August 19, 2004


Adrienne Chocholak



PATENT APPLICATION
Docket No. 8750-040
Client No. SPX200211-0043US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Jae-Hun Kim and Kyu-Whan Chong

Serial No. 10/666,507 Examiner: Perkins, Pamela E.

Filed: September 19, 2003 Art Unit: 2822

For: MOS TRANSISTOR HAVING A RECESSED GATE
ELECTRODE AND FABRICATION METHOD THEREOF

Confirmation No. 9186

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO RESTRICTION REQUIREMENT

Responsive to the Restriction Requirement, dated July 23, 2004, Applicant
hereby elects Group I (claims 1-6), without traverse.

Customer No. 20575

Respectfully submitted,

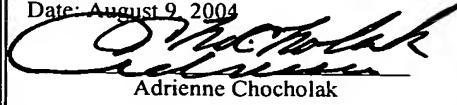
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